

IT AND TRADEMARK OFFICE

Appl. No.:

Liang et al 09/089,290

Filed:

June 1, 1998

Title:

Reduced Resolution V

Art Unit:

2713

Examiner:

Lee

Docket:

TI-26415

Technology Center 2600

Assistant Commissioner for Patents

Box NonFee Amendment Washington, DC 20231

MAILING CERTIFICATE

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 today.

Gracia Sansom

Date

AMENDMENT AND/OR ARGUMENT

Dear Sir:

In response to the Office Action mailed 08/05/02, applicants hereby request reexamination and reconsideration and argue as follows

Claims 1-5 were rejected as anticipated by Civanlar. The Examiner asserted that the decoder of Fig.4 decodes a first macroblock of predicted frame 401 at a first resolution and decodes a second macroblock using upsampling of stored reference macroblock 409 at a second resolution.

Applicants point out that Civanlar item 401 is a partitioned buffer containing plural images of varying resolution but which are treated as a single image by standard MPEG decoder 402 and decoded as such (column 11, lines 50-60). That is, MPEG decoder 402 does not change decoding resolution during its decoding as required by claim 1. Further, Civanlar item 409 is not a stored reference macroblock but rather part of compositor 407 which rearranges the output of MPEG decoder 402; that is, 407 does not do decoding and item 409 is a (decoded) line segment mapping table. Civanlar has no suggestion of the decoding of independent claim 1; and the claims are patentable over the reference.

Respectfully submitted,

Carlton H. Hoel Reg. No. 29,934

Texas Instruments Incorporated PO Box 655474, M/S 3999 Dallas, Texas 75265

972.917.4365